

Non-profiled Mask Recovery: the impact of Independent Component Analysis

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Outline

Introduction

Independent Component Analysis in SCA

Case I: Table Re-computation Schemes

Case II: DPAContest v4.2

Conclusion



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Side Channel Analysis

SCA

- Attacks based on information leakage (timing, power consumption, electromagnetic emission, etc.)
- Kecover the secret key within a few minutes (1 several million traces)



Figure: Side Channel Analysis



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Conventional (non-profiled) SCA

A typical "guess-and-determine" procedure

- K Take a guess on a fixed secret (eg. the partial key k)
- Ke Statistic calculations (distinguishers) on the leakages
- ${\it k}{\it k}$ Find the most likely key guess k



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Conventional (non-profiled) SCA

Eg. a typical HW-CPA on AES, with $T \ensuremath{\mathsf{power}}$ traces



Figure: Conventional SCA: Step 1



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Conventional (non-profiled) SCA

Eg. a typical HW-CPA on AES, with $T \ensuremath{\mathsf{power}}$ traces

 ${\it k}{\it k}$ Step 2: Compute the intermediate states from the plaintexts and k



Figure: Conventional SCA: Step 2



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Conventional (non-profiled) SCA

Eg. a typical HW-CPA on AES, with T power traces

K Step 3: Compute the expected leakages as M(x) = HW(x)



Figure: Conventional SCA: Step 3



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Conventional (non-profiled) SCA

Eg. a typical HW-CPA on AES, with T power traces

Ke Step 4: Find out the key guess with the highest correlation coefficient



Figure: Conventional SCA: Step 4



Countermeasure: random masking

A 1st-order masking scheme splits the intermediate state x into (x_m, m) , where

- $\not k x_m \oplus m = x$
- \bigstar mask m changes after each encryption

Therefore, the attacker's options are:

- Ke Recovering m first
- \checkmark "Cancel" m by combining multiple leakages (a.k.a. higher order attack)



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Recovering random masks

Profiled attacks:

- \checkmark Match the leakage of m with pre-built templates
- ${\it k} {\it k}$ One valid trace for each m
- Ke Possible, yet not as powerful as conventional key-recovery

Non-profiled attacks:

- Ke Cannot "guess-and-determine": only one trace for each m (v.s. the fix secret k)
- \mathbf{k} Cannot recover m in general
- Ke However, under certain circumstances...





Non-profiled mask recovery

"Horizontal attacks" on the "table re-computation" procedure:

- \checkmark Use all 2^n leakages of $m, m \oplus 1, ..., m \oplus (2^n 1)$ (n: bit-width of the Sbox)
- ${\it k}$ Verify guesses of m with all the "horizontal" information on this trace

 \mathbf{k} Same as a vertical SCA with 2^n traces







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Non-profiled mask recovery

Previous "Horizontal attacks":

- K AES on 8-bit microcontroller, 900 traces [WISA 09]
- Ke AES on 8/32-bit microcontroller, with several hiding techniques [FSE 13]
- K Need enough leakage samples for "guess-and-determine" (*n* is large enough)

Can we do better than this?



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Goal: Learn S from Y

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Independent Component Analysis (ICA)

A typical example of "Blind Source Separation (BSS)":

- $\bigstar \text{ Blind sources } \mathbf{S} = (s_1, s_2, ..., s_n)$
- 🖌 Linear mix matrix ${f A}$
- $\bigstar \ m \text{ observations } \mathbf{Y} = (y_1, y_2, ..., y_m)$
- $\mathbf{k} \mathbf{Y} = \mathbf{A} * \mathbf{S} + \mathbf{N}$ (N represents the noise)



Figure: Blind Source Separation





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ICA in side channel analysis

Denoising:

- Idea/initial discussion [e-SMART 03]
- AES traces on software/hardware, with/without countermeasure [COSADE 18]

Other applications (all related to non-profiled state recovery):

- K Side Channel Analysis based Reverse Engineering [CT-RSA 17]
- K Middle round attack [CT-RSA 17]



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ICA & Random masks

Rationale

- Unlike conventional SCA, ICA recovers the "changing secrets"
- Ke The following key recovery should be easy

Technical Issue

- K Constructing multiple "ICA observations"
 - ► To recover an *n*-bit state x, ICA needs n different leakages $(L_1(x), ..., L_n(x))$



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Table re-computation schemes

For a *d*-order masking scheme,

- Ke Compute the masked table according to the first d-1 shares
- ${\it k}$ Look up the last share $x^{(d)}$



Figure: A *d*-order table re-computation scheme



Utilising the 2^n table look-ups

Without loss of generality, let us assume the leakage for look up x is L(x):

- ${\it \ \ } {\it \ \ } 2^n \text{ leakage samples available: } (L(x), L(x\oplus 1),...,L(x\oplus 2^n-1))$
- ₭ Horizontal CPA[WISA 09, FSE 13]:
 - Guess the mask share x
 - Recover x with conventional HW-CPA
- 🖌 ICA-based recovery
 - If L is linear, $L(x \oplus c)$ can be regarded as $L_c(x)$ [CT-RSA 17]
 - ICA takes n constants c-s to recover x



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Horizontal attacks v.s. ICA

Horizontal attacks are mainly restricted by n

- 🖌 Leakage model
 - Horizontal attacks: L can be anything, but most likely HW
 - ICA: L must be linear (i.e. weighted HW)
- Ke Sample size ("the number of traces")
 - Horizontal attacks: 2^n available, enough for n = 8 [WISA 09, FSE 13]
 - ICA: 2^n available, in theory only takes n



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Experimental Validation

Acquisition Setup:

- K Target: IC card with 8-bit microprocessor (Atmega163)
- ✗ PicoScope 3206D, running at 1GSa/s
- \checkmark 2-shares implementation of the Sbox of PRESENT (n=4)
- № 200 traces, with 2M samples on the trace
- 🖌 Testing attacks
 - Horizontal CPA with HW model
 - ICA-based mask recovery



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Experimental Validation

- K Lack of samples for horizontal CPA (\sim 30% mask recovery)
- Keven ICA-based mask recovery works well (\sim 20 traces for key recovery)



Figure: Horizontal CPA v.s. ICA



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The Rotating Sbox Masking Scheme (RSM)

A type of Low-Entropy Masking Scheme (LEMS)

- $m{\Bbbk}$ A fix 16B mask set M
- ${\it k}{\it k}$ Choose a 4-bit random offset x and use M[x] as the 1st round mask
- $\label{eq:masked} \textbf{\texttt{16 pre-computed}} \ \text{masked tables (mask } M[x] \ \text{to mask } M[x+1] \textbf{)}$
- $\label{eq:main_state} \hbox{\tt K} \ \, {\rm The} \ (r+1) \text{-th round is} \ \, M[(x+r) \mathrm{mod} \ 16]$





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The Rotating Sbox Masking Scheme (RSM)

No table re-computation, but:

 \checkmark In each round, the random offset x must be loaded from memory

 $\label{eq:leakage} \hbox{\tt keakage of } (x+r) \hbox{\tt mod } 16$

Algorithm 3 ASM codes of the masked Sbox computation in DPAContest v4.2		
1: ldi YH,hi8(offset)		▷ point to the offset array location
2: ldi YL,0x00		
ld offset, Y	Generate the leakage	\triangleright load offset x
4: ldi ZH, hi8(aes_sbox0)		
5: add offset,I2		$\triangleright x = x + r$
6: andi offset,0x0F		$\triangleright x = x \mod 16$
7: add ZH, offset		Determine the masked table
8: clr ZL		
9: mov ZL, ST11		▷ Table look up
10: clr ST11		
11: lpm ST11, Z		



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Utilising such leakage...

Same strategy as before

- $\label{eq:loss_loss} \begin{array}{l} \hbox{\rlap{k}} 10 \text{ leakage samples available} \\ (L(x), L((x+1) \mathrm{mod} \ 16), ..., L((x+9) \mathrm{mod} \ 16))) \end{array}$
- 🖌 Horizontal CPA
 - Guess the mask share x
 - Recover x with conventional HW-CPA (only 10 samples!)
- 🖌 ICA-based recovery
 - $L((x+r) \mod{16})$ cannot be regarded as $L_r(x)$
 - For certain r-s (1,2,4,8), can be approximated by $L(x\oplus r)$
 - Price to pay: adding more algorithmic noise



Experimental Validation

EM traces from DPAContest V4.2,

- K Lack of samples for horizontal CPA (\sim 15% mask recovery)
- \checkmark ICA-based mask recovery works well (\sim 30 traces for key recovery)



Figure: Horizontal CPA v.s. ICA



Other attacks

Compared with the hall of fame of DPAContest v4.2:

- Not the most efficient attack
 - Non-profiled: 14 traces
- Not really a fair comparison
 - ICA does not utilise the "Implementation-specific" information (eg. target register, bus, etc.)
 - Hard to fix by cautious implementations

A straightforward way to avoid accidental computation of the form $m_i \oplus m_{i+1}$ in the implementation flow, is to rewrite the complete code in assembly language. However writing assembly code is a tedious and error-prone task. A common practice is to <u>write only the sensitive modules of the code in assembly</u>. This is considered as best practice to avoid any surprises from complication. Another precaution which must be taken at this stage is register precharge. If we precharge every general purpose register to '0' value before writing in a <u>new value</u>, we can avoid all leakages of form $m_i \oplus m_{i+1}$. By ensuing these two



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Take home message

- ICA-based mask recovery works for some masking schemes
- K A "pre-trace" changing secret is not always intrinsically secure
 - Profiled: template attacks suppose the masks can be profiled
 - Non-profiled—horizontal: having a lot leakages of the same secret helps
 - Non-profiled—ICA: the secret can possibly be recovered even if there are only a few leakage samples available



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Acknowledgement









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Questions?

